

Date: 21/10/2025

Corrigendum01

This is with reference to the tender notification for “**Altera FPGA Kits**”, Tender Reference No: NITK/EC/OH35-IRG/FPGA/Q/671/2025 Dated:26/09/2025, there are following corrections:

1. Last Date and Time for submission of tender is extended up to 3.00 p.m. on 24/10/2025
2. Date of opening of technical bid: 24/10/2025 @ 4:00 p. m.

Sd/-
Buyer
(Dr. Laishram Thoibileima Chanu)

Sd/-
HOD Dept of ECE
NITK, Surathkal