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NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल

P.O SRINIVASNAGAR, MANGALORE - 575025

EE Call Letter List

M Tech. (Research), 2023 - 24

NITK / EE D1 / 1232

Subject: EE call letter

Date: 29/05/2023

The following applicants have been selected for written exam and/or interview for the department for the department of Electrical and Electronics Engineering for M Tech. (Research) Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	MIR BURHAN UR REHMAN	MTS2023PS0002	Power & Energy Systems
2	MALLOTH PRAVEEN PRAKASH	MTR2023PEV0003	Power Electronics and Control for Electric Vehicle

 29/05/2023
Head Of Department

Electrical and Electronics Engineering

ASSOC. PROFESSOR & HEAD

DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA

SURATHKAL SRINIVASNAGAR P.O., MANGALORE-575025

Dean Academics

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL,
SRINIVASNAGAR, MANGALORE – 575 025**

Tel : 0824-2473045, 2474054

Fax : 0824-2474039

Website : www.eee.nitk.ac.in

No.: NITK/EED/ 1237

29th May 2023

OFFICIAL MEMORANDUM


Sub: M.Tech(R/S) Programme - Selection / Admission for the Year 2023; Intimation for Interview.

With reference to his/her application for admission to M Tech (R/S) Programme, you are requested to appear for the interview at NITK Surathkal. Please produce all the original records such as Date of Birth Certificate, Degree Marks Cards, SC/ST/OBC/EWS certificate (if applicable as per proforma), Person with disability certificate (if applicable), Sponsorship letter (if applicable) and Conduct Certificate during the interview.

Department	: Department of Electrical and Electronics Engineering (E & E)
Interview Date	: 19-06-2023
Registration	: 8.30AM at Seminar Hall, Dept. of E & E, NITK, Surathkal
Document Verification	: 8.30AM at Seminar Hall, Dept. of E & E, NITK, Surathkal
Place of Interview	: Conference Room, Dept. of E & E, NITK, Surathkal
Interview Time	: 10 AM onwards at Conference Room, Dept. of E & E

NOTE:

1. Please see the institute eligibility criteria given in the advertisement, carefully. It is the sole responsibility of the candidate to ascertain his/her eligibility before coming/appearing for the interview. The final confirmation of your eligibility is done only after the verification of original certificates.
2. The candidates are requested to go through the list of instructions which is enclosed herewith.
3. Candidates must bring the “**Data Collection Format**” duly filled and should be submitted at the time of Document verification on 19th June 2023.
4. Full-time/External Registrants - sponsored from Industry or other organizations, should have been serving in the sponsoring organisation for a period of **at least 2 years** after qualifying degree and have to produce a letter from their employer stating that the candidate is deputed for M Tech / M Tech (Research) in the Institute on full salary during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).
5. Candidates who have not submitted marks of final examination along with application form shall produce the same at the time of admission if available. However, candidates who have written final year examinations and yet to obtain final semester mark cards should submit the same on or before **30.09.2023**.
6. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in the Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage
7. Please note that no expenses shall be payable for appearing in the Interview
8. The Selected candidates are required to pay fees and get admitted to the Institute as per the dates specified by the Institute. Fees details are given in the Information Bulletin 2023-24 (M Tech & PhD)


ASSO. (HoD & Chairman DPGC, EED)
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA
SURATHKAL SRINIVASNAGAR P.O., MANGALORE-575025

Date: 29th May 2023

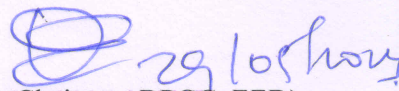
INSTURCTIONS TO CANDIDATES

Dear Candidates,

You are invited to attend the interview for the M.Tech.(R)/ M. Tech (Spons) programme in our Department. Here are few more instructions to make it convenient to all of us.

1. The following Certificates shall be brought in ORIGINAL for verification purposes.
 - (a) GATE Score Card
 - (b) B.E / B.Tech Marks Cards and Degree Certificates
 - (c) X and XII marks cards
 - (d) Caste Certificate (if applicable)
 - (e) Valid proof of photo identity.

(Note : For OBC, the certificate must be dated on or after 1st April 2023. Please make sure that the caste name appear in the central list published by National Commission of Backward Classes (NCBC) Govt. of India at the link : www.ncbc.nic.in. **If the caste is not listed by NCBC or the certificate date is prior to 01/04/2023, then the OBC candidate will be considered under Open Category Only.** The OBC certificate format is available through the institute website (www.nitk.ac.in). For EWS category, the certificate should be as per the prescribed format issued by the competent authority on or after 01/04/2023.
2. Please visit the Department Website (www.eee.nitk.ac.in) for updates and instructions which will be released by the department from time to time. The Department will not entertain any such concerns by the candidates regarding unawareness of the updates and instructions published in the department website.
3. Non-Production of Original Documents for Verification Purposes will disqualify the candidate from selection process.
4. The procedure for selection is given on Page 3 of this document. The Candidates are required to go through the procedure carefully before appearing for the selection process.
5. The candidates are informed to download and fill up the Data Collection Format (Page 4 to 6) carefully and submit during Document Verification. This data will be used for preparation of the merit list.
6. The Candidates are required to produce reprints of their publications, original certificates of work-experience and training-undergone at the time of Interview.
7. The Candidates are required to be available on **19th June 2023** at Dept. of E & E, NITK, Surathkal for completion of the selection process and be available on mobile call for receiving and providing necessary information's.
8. The candidates have to comply with the instructions published in the information bulletin for Ph.D & M.Tech Programme 2023-24, regarding admission procedure as stipulated by the Institute.
9. For any clarifications contact Dr.R.Kalpana, Secretary, DPGC, through e-mail: kalpana@nitk.edu.in or Ph: +91-824-2473468 (Office), +91-8762245244 (Mob).


AS (HoD & Chairman DPGC, EED)
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA
SURATHKAL SRINIVASNAGAR RD, MANGALORE-575025

Admission to M. Tech (R) and M. Tech (Sponsored) programme, July 2023 -24 Session

Selection Procedure:

Factors considered for selection: The selection of the scholars is based on all the following components considered at different stages as indicated in the *Procedure of Selection*:

1. Application in the prescribed format with all necessary data and submission of further information/ data sought by the department at the time of registration and interview.
2. Performance at the Undergraduate level (considering the present status of applicants CGPA).
3. GATE Score.
4. Performance in the interview.
5. Experience and publications to be assessed during the interview.

Procedure of Selection (at the department level): The selection will involve the following stages:

Stage-1: All eligible candidates will be allowed for the document verification.

Stage-2: A merit list of candidates will be formed by considering the performance at the UG and GATE Score. The weightages for different components are as follows:

UG performance (1 st to 7 th semesters)	GATE Score	Total
30 %	70 %	100%

Stage-3: The final short-listed candidates in **Stage-2**, will be interviewed by the Departmental Post Graduate Committee (DPGC). Based on the performance of candidates in the interview, the DPGC will prepare a list of candidates recommended for admission to M. Tech (R/S). This list will be submitted to the Dean Academic for approval. This may be a NULL list in case no candidate is found suitable.

Procedure of short-listing of candidates for Interview (Institute Scholarship) after Stage-2:

Step-1: If the department has N number of seats in the Open Category (OC), then the cut-off mark (M1) for OC will be the mark of (3N)th candidate in the merit order formed in Stage-2. This list (L1) may be represented by the candidates from all categories (OC, OBC, SC, ST and PWD).

Step-2: The cut-off mark (M2) for OBC candidates is fixed at 90% of the Cut-off mark (M1) for OC. This list is L2 and contains all OBC candidates having score between and including M1 and M2 but limited by 2 times the number of seats available in OBC category.

Step-3: The cut-off mark (M3) for SC candidates is fixed at 75% of the Cut-off mark (M1) for OC. This list is L3 and contains all SC candidates having score between and including M1 and M3 but limited by 2 times the number of seats available in SC category.

Step-4: The cut-off mark (M4) for ST candidates is fixed at 70% of the Cut-off mark (M1) for OC. This list is L4 and contains all ST candidates having score between and including M1 and M4 but limited by 2 times the number of seats available in ST category.

Step-5: The shortlist of candidates for the interview is $L=L1 \cup L2 \cup L3 \cup L4$ (U - Union).

Data Collection format
Admission to M.Tech (Res/Spons) Programme for the Academic Session July-Dec. 2023

Name of the candidate				Application No/Registration No.			
Email id:				Mobile No.			
Under Graduate		Awarded Marks / Grades		Equivalent % age marks (A=)			
	UG (Up to 7 semesters)	CGPA (C3G)		$(C3G-0.5) \times 10$			
		Percentage (C3)		C3			
	UG (Aggregate of all semesters)	CGPA (C4G)		$(C4G-0.5) \times 10$			
		Percentage (C4)		C4			
GATE(* Declared out of 100 in GATE score card)		Year	Marks(*)		Marks Normalized (*)		
10th Level (CGPA/ Marks)				12th level (CGPA/ Marks)			
Diploma (CGPA/ Marks)							
Remarks (≥ 6.5 CGPA/60% for OC/OBC) (≥ 6.0 CGPA/55% for SC/ST) @ UG level			QUALIFIED		NOT QUALIFIED		
Experience and Publications to be considered during the interview							
Experience (in years)		Teaching	Industry		Others (Specify)		Remarks
Publications (Numbers)		Journals	Conferences		Books		Remarks

Declaration by the Candidate: I hereby state that I have understood all the instructions provided so far and that I have taken all the necessary care in providing the information in the Data Collection Form. I will be solely responsible for all the consequences arising out of submission of incorrect data, the one that is being used in the selection process.

Signature of the candidate
Date: 19-06-2023
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

UG (Semesters 1 to 4) (First Attempt Grades/ Marks only to be used)

Name of the candidate							Application No						
Sem.	Course Details			Results			Sem.	Course Details			Results		
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
1							2						
3							4						

Signature of the candidate
Date: 19-06-2023
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

UG (Semesters 5 to 8) (First Attempt Grades/ Marks only to be used)

Name of the candidate							Application No						
Sem.	Course Details			Results			Sem.	Course Details			Results		
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
5							6						
7							8						

Signature of the candidate
Date: 19-06-2023
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff