

राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल P.O SRINIVASNAGAR, MANGALORE - 575025

EC Call Letter List

M Tech. (Research), 2022 - 23

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for M Tech. (Research)

Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	Shristi Chandrakar	MTR2022SP0001	Signal Processing and Machine Learning
2	Kodgire venkatesh vilas	MTR2022VL0006	VLSI Design
3	Keerthana Vudumula	MTR2022VL0007	VLSI Design
4	Keerthana Vudumula	MTR2022CN0002	Communication Engineering and Network
5	Nikhil Jinagouda Shedshale	MTR2022VL0008	VLSI Design
6	Prathyusha M R	MTR2022SP0004	Signal Processing and Machine Learning
7	KULDEEP DUTTA	MTR2022VL0009	VLSI Design
8	KULDEEP DUTTA	MTR2022CN0003	Communication Engineering and Network
9	Hitesh Malviya	MTR2022VL0010	VLSI Design
10	SANAMPUDI GOPALA KRISHNA REDDY	MTR2022VL0012	VLSI Design
11	KULDEEP DUTTA	MTR2022SP0006	Signal Processing and Machine Learning
12	Rakesh C	MTR2022CN0004	Communication Engineering and Network
13	PONNADA CHATRAPATHI SRS KRISHNA	MTR2022VL0016	VLSI Design
14	Isha Deepak Naik	MTS2022VL0019	VLSI Design

Asla: 3-6-2022

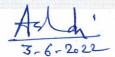


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M Tech. (Research), 2022 - 23

15	Isha Deepak Naik	MTS2022SP0007	Signal Processing and Machine
#	Name	Number	Branch/Specialisation
16	Isha Deepak Naik	MTS2022CN0007	Communication Engineering and Network
17	Vikas Goyal	MTR2022VL0021	VLSI Design
18	Anushka Tripathi	MTR2022VL0022	VLSI Design
19	KOSHIKA	MTR2022VL0025	VLSI Design
20	Nomula Taraka Sada Siva Srinivas	MTR2022VL0027	VLSI Design
21	Kaushambi	MTR2022VL0029	VLSI Design
22	Saud Akhtar	MTR2022VL0030	VLSI Design
23	SALINI YADAV	MTR2022VL0031	VLSI Design
24	PAPPU KUMAR	MTR2022VL0032	VLSI Design
25	Leslie	MTR2022VL0033	VLSI Design
26	PAPPU KUMAR	MTR2022SP0013	Signal Processing and Machine Learning
27	Ankit Kumar	MTR2022VL0034	VLSI Design
28	Ankit Kumar	MTR2022CN0011	Communication Engineering and Network
29	Ankit Kumar	MTR2022SP0014	Signal Processing and Machine Learning
30	SHOBHIT BHATT	MTR2022SP0015	Signal Processing and Machine Learning
31	OMKAR DEOKINANDAN NAYAK SHINKRE	MTR2022VL0035	VLSI Design
32	Arpit Kumar	MTR2022VL0036	VLSI Design
33	Siddhant Mahendra Pagare	MTR2022VL0038	VLSI Design
34	Vignesh M	MTR2022VL0039	VLSI Design



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35 #	Vignesh M Name	MTR2022CN0012 Reference Number	Communication Engineering and Network Branch/Specialisation
<i>"</i> 36	Vignesh M	MTR2022SP0017	Signal Processing and Machine Learning
37	AADARSH V	MTR2022VL0040	VLSI Design
38	POOJARI NAGA PRADEEP	MTR2022VL0042	VLSI Design
39	Harini Krishna Priya Pothula	MTR2022SP0018	Signal Processing and Machine Learning
40	SALINI YADAV	MTR2022SP0019	Signal Processing and Machine Learning
41	SALINI YADAV	MTR2022CN0013	Communication Engineering and Network
42	Vishnu K K	MTR2022SP0020	Signal Processing and Machine Learning
43	Rahul Sharma	MTR2022VL0044	VLSI Design
44	Radheshyam Manojkumar Sharma	MTR2022VL0046	VLSI Design
45	KORLEPARA SAI VIJAY ROHIT	MTR2022VL0047	VLSI Design
46	AMAN PATEL	MTS2022VL0051	VLSI Design
47	Abin Francis	MTR2022VL0057	VLSI Design
48	Ashish Kumar Singh	MTR2022VL0058	VLSI Design
49	KOUDA SHIVA GANESH	MTR2022VL0059	VLSI Design
50	Prakash Dev	MTR2022SP0022	Signal Processing and Machine Learning
51	Prakash Dev	MTR2022VL0060	VLSI Design
52	KANISHKA RAVI MADISHETTY	MTR2022VL0061	VLSI Design
53	Ashish Kumar Singh	MTR2022SP0023	Signal Processing and Machine Learning

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54	Darshith Jain B S	MTR2022VL0064	VLSI Design
¥ 5	Name Chandrakar	NUR2022VL0065	Brah@h\$Specialisation
56	SAGARIKA MANDAL	MTR2022VL0067	VLSI Design
57	Chandrima Upadhyay	MTR2022VL0068	VLSI Design
58	ALOK KUMAR VINAY KUMAR SHUKLA	MTR2022VL0070	VLSI Design
59	ULASALA ABHISHEK	MTR2022VL0071	VLSI Design
60	KOSHIKA	MTR2022SP0027	Signal Processing and Machine Learning
61	NILKAMAL KUMAR	MTR2022VL0072	VLSI Design
62	Bibhay Kumar	MTR2022CN0019	Communication Engineering and Network
63	Aviral Shrivastava	MTR2022VL0074	VLSI Design
64	ADITI	MTR2022SP0029	Signal Processing and Machine Learning
65	Patil Yashkumar	MTR2022VL0077	VLSI Design
66	Alok Vinod Kumar Singh	MTR2022VL0078	VLSI Design
67	Vijay Narasimha	MTR2022CN0021	Communication Engineering and Network
68	Vijay Narasimha	MTR2022SP0030	Signal Processing and Machine Learning
69	Deepesh Singodiya	MTR2022VL0082	VLSI Design
70	Vaibhavi Meena	MTR2022VL0083	VLSI Design
71	PARMEET ROOPRAI	MTR2022VL0084	VLSI Design
72	Himanshu surendraprasad behera	MTR2022VL0086	VLSI Design
73	Sakshi Rathore	MTR2022VL0088	VLSI Design
74	CHANDRIKA C KULKARNI	MTR2022VL0090	VLSI Design



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M Tech. (Research), 2022 - 23

75 #	CHANDRIKA C KULKARNI Name	MTR2022CN0024 Reference Number	*Communication Engineering and Network Branch/Specialisation
76	Suhit Hemant Telang	MTR2022SP0035	Signal Processing and Machine Learning
77	Suhit Hemant Telang	MTR2022VL0091	VLSI Design
78	Suhit Hemant Telang	MTR2022CN0025	Communication Engineering and Network

Head Of Department

Electronics and Communication Engineering Translation Engineering Translation

मंगेलूर / MANGALORE - 575 025

Dean Academics

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL

P.O. SRINIVASNAGAR, MANGALURU-575 025 Telephone: 0824-2473046, Website: www.ece.nitk.ac.in

Date: 03-06-2022

Shortlisted candidates for M Tech (R/S) Programme-Written Aptitude Test / Interview for the year 2022-23

With reference to your application for admission to M Tech (R/S) Programme in the department of Electronics and Communication Engineering, you are requested to appear for the Written test/Interview at NITK Surathkal. You should produce all the original records such as Date of Birth Certificate, Degree Certificate and Marks Cards of all semesters (UG programme), SC/ST/OBC/EWS certificate (if applicable as per proforma), Person with disability certificate (if applicable), Sponsorship letter (if applicable), Conduct Certificate and valid photo identity card. Please keep a self-attested photocopy of all these certificates readily available at the time of interview.

Department : Department of Electronics and Communication Engineering

Place of Reporting : Department of Electronics and Communication Engineering, NITK Surathkal

Written Test Date and Time: June 16, 2022, 9.00 AM at ECE Department

Announcement of shortlisted: June 16, 2022, 1.00 PM

candidates for interview

Interview Date and Time : June 16, 2022, 2.00 PM Onwards, Meeting room, ECE Department

June 17, 2022, 9.00 AM Onwards, Meeting room, ECE Department

NOTE:

- 1. Candidates should be prepared to appear for a written Aptitude Test before the interview.
- 2. Candidates should be prepared to appear for the written Aptitude Test before the interview. Fee Structure for M Tech (S/R) programme and Course syllabus for aptitude test are provided on Institute's website, i.e. www.nitk.ac.in. A copy of the syllabus for the aptitude test is given in a separate link.
- 3. Full-time/External Registrants sponsored from Industry or other organizations, should have been serving in the sponsoring organisation for a period of <u>at least 2 years</u> after qualifying degree and have to produce a letter from their employer stating that the candidate is deputed for M Tech / M Tech (Research) in the Institute on full salary during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form)
- 4. Candidates who have <u>not submitted</u> marks of final examination along with application form shall produce the same at the time of admission if available. However, candidates who have written final year examinations and yet to obtain final semester mark cards should submit the same on or before **30.09.2022**.
- 5. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in the Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage
- 6. Please keep the Admit Card ready during the online test and interview. You are responsible for safe custody of the Admit Card and in the event of any other person using this Admit Card, the responsibility lies on you to prove that you have not used the service of an impersonator.
- 7. Please note that no expenses shall be payable for appearing in the written test/Interview.
- 8. The Selected candidates are required to pay fees and get admitted online during the period 29.06.2022 to 06-07-2022.

Head of the Department

THE MANGALORE 575 025