



**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA
SURATHKAL, SRINIVASNAGAR - 575025
MANGALORE, KARNATAKA, INDIA**

RefNo: NITK/E&E ISRO/DDM/2021-22/A8

Date: 12/04/2022

Advertisement for Junior Research Fellow (JRF)

Applications are invited from eligible candidates for the post of **Junior Research Fellow** to work under the research project funded by [Indian Space Research Organisation, Govt of India](#) under RESPOND scheme through RAC-S. As a part of this role, the JRF will carry out cutting edge research in the area of power electronics for the development of power converter. The work will include simulation studies and hardware implementation. The JRF is expected to carry out high impact research, publish and present results into conferences/workshops and journals in the area. Details follow.

Project title and Reference No: Design and Development of Multi Input/Multi Output Power Converter. (Ref. No: ISRO/RAC-S/NITK/2021-22, dt: 01/12/2021)

Principal Investigator:

Dr. A. Karthikeyan

Department of Electrical and Electronics Engg.,

NITK Surathkal, Mangalore, India

E-mail: jakarthik@nitk.edu.in

Essential Qualifications

BE/B.Tech in Electrical & Electronics Engineering and M.Tech/MS(R)/M.E./equivalent in Electrical & Electronics Engineering with expertise in Power Electronics. Candidates must have at least 6.5/10 CGPA or 60 percent marks in aggregate from a recognized technical institute or university as a full-time program. Proof of B.Tech/B.E./M.Tech./M.E./MS(R)/equivalent certificate has to be provided during the time of interview.

These are the minimum eligibility criteria. The short-listing criteria may vary according to the number of applications received.

Age Limit: 28 years. **Age relaxation will be provided as per ISRO, GoI norms.**

Desired Skills

- Sound knowledge in the area of power electronics converter configurations and controls.
- Experience in modeling, design and control of Power Electronic Converters.
- Experience in developing and working on hardware / laboratory prototype of the converters.
- Knowledge of simulation soft-wares like MATLAB (programming/SIMULINK).
- Candidate must be well conversant to programming with different digital controllers.

Salary and Duration

- INR 31000 per month (consolidated)+HRA (as per norms).
- The selected candidate will be appointed initially for one year (on contract) and the services will be extended for next year based on the performance review and is co-terminus with the project.

How to apply/Application Process

Interested candidates must apply with the following documents: (1) Cover letter (2) Filled in application (3) Bio-data with passport-sized photograph, (4) Scanned copies of educational certificates and mark sheets, class X onwards (5) GATE qualified certificate and (6) Scanned copies of Proof for research experience, special achievements and publications, if any. The soft copies of all the above documents (pdf format) must be emailed to the Principal Investigator (Dr. A. Karthikeyan, Email id: jakarthik@nitk.edu.in) on or before 22nd April 2022. **No TA/DA will be paid for attending the written test/interview. The appointment will be on a purely temporary basis. Candidates who are already employed should produce relieving certificate from their employers, if selected.** The shortlisted candidates with the date of written test/interview will be intimated by email only.

NITK Surathkal reserves the right to reject any or all the applications without assigning any reasons thereof.

Address for correspondence:

To,
Dr. A. Karthikeyan
Principal Investigator,
Department of Electrical & Electronics Engg.,
NITK Surathkal, Mangalore-575025, India.
Mobile : +918867236461
E-mail: jakarthik@nitk.edu.in

Note : The e-mail should be sent with the subject line “**Application for JRF for project under RAC-S**”, with Advt. No: NITK/E&E ISRO/DDM/2021-22 dated 28-March-2022



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MANGALORE,KARNATAKA,INDIA**

**Application for the Position of Junior Research Fellow
for project under RAC-S**

1. Post Applied for: JRF-Junior Research Fellow

2. Name of the Candidate(BLOCKLETTER):

3. Father's Name(BLOCKLETTER):

4. Mother's Name(BLOCKLETTER):

5. (a)Date of Birth:(DD/MM/YYYY)_____

(b) Sex(Male/Female/Other):____

(c) Marital Status: Married/Single

(d) Category:SC/ST/OBC/PWD/Open

6. Previous Research experience:(use additional sheet if required)_____

7. Publication(s),if any:(use additional sheet if required)_____

8. GATE/NET(if any):Qualified(Yes/No):Score:_____Rank:_____

Specialization:_____Year: _____

Paste here a
recent passport
size photograph

9. Academic Qualification:(Starting from Standard10th or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/CGPA

10. (a)Address for Communication:(BLOCKLETTER):

(b) Contact No(Mob):

(c) E-mail ID:

11. Contact Details of two referees:

	Referee I	Referee II
Name:		
Designation:		
Organization:		
OfficeAddress:		
OfficePhone Number:		
EmailID:		

12. Declaration:

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

Signature

Note-If any information is required, extra A4 sheet may be attached.