National Institute of Technology Karnataka (NITK), Surathkal
Srinivasnagar – 575 025, (Dakshina-Kannada)
Karnataka, India

Visvesvaraya PhD Scheme – Phase II

NITK/VIS_PDSC/2026/PDSC/3 **Date: 18-Nov-2026**

NOTICE

Visvesvaraya PhD Scheme for Electronics and IT: Phase II – Post-Doctoral Fellowship, AY 2026-27: List of candidates shortlisted for presentation and interview

With reference to the call for applications for the Post-Doctoral Fellowship under the Visvesvaraya PhD Scheme, Phase II, the following candidates, listed by IRIS application number, have been shortlisted for the presentation and interview.

Please note: there is only **one (1) Post-Doctoral Fellowship position** in total, across the five participating departments (CSE, ECE, EEE, IT and MACS). All shortlisted candidates will be assessed together for this single position.

Sl. No.	IRIS reference no.	Candidate name	Department
1	NITK2026PDF0003	Charu Singh	Electronics and Communication Engineering
2	NITK2026PDF0004	Vineetha Pais	Information Technology
3	NITK2026PDF0005	Vineetha Pais	Mathematical and Computational Sciences
4	NITK2026PDF0007	ASIYA	Computer Science and Engineering
5	NITK2026PDF0009	Vishakha Vaishnav Dixit	Electronics and Communication Engineering
6	NITK2026PDF0010	Vishakha Vaishnav Dixit	Electronics and Communication Engineering
7	NITK2026PDF0011	Vishakha Vaishnav Dixit	Electrical and Electronics Engineering
8	NITK2026PDF0012	Pranav George	Mathematical and Computational Sciences
9	NITK2026PDF0018	Alok Kumar	Electronics and Communication Engineering
10	NITK2026PDF0021	Neema K	Electronics and Communication Engineering
11	NITK2026PDF0025	MEKA KALYAN CHAKRAVARTHI	Electrical and Electronics Engineering
12	NITK2026PDF0028	Praveen Gugulothu	Computer Science and Engineering
13	NITK2026PDF0029	LENIN MARKSIA U	Mathematical and Computational Sciences
14	NITK2026PDF0030	Shenoy Meenakshi Mahesh Jyoti	Mathematical and Computational Sciences
15	NITK2026PDF0031	Anilkumar Boya	Electrical and Electronics Engineering
16	NITK2026PDF0033	MANGALA SHENOY K	Mathematical and Computational Sciences
17	NITK2026PDF0037	Soumyadeep Maity	Electrical and Electronics Engineering

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Schedule

Stage	Date	Reporting time	Venue
Presentation and interview	Thursday, 10 December 2026	08:30 AM	Main Building, NITK Surathkal
Final interview (candidates recommended after the first stage)	Friday, 11 December 2026	9:00 AM	Main Building, NITK Surathkal

Instructions to candidates

1. Individual call letters with the time slot and presentation format are being sent by email to the address given in the IRIS application.
2. Candidates must confirm their attendance by replying to the call letter by 20-11-2026 (Friday).
3. Candidates must attend in person. Online participation is permitted only for candidates residing abroad, with prior approval.
4. Candidates must bring the original certificates (all degrees and marks cards, PhD degree or provisional certificate, and proof of date of birth) and one set of self-attested copies for verification.
5. No travel allowance or accommodation will be provided for attending the interview.
6. The schedule is tentative. Any change will be posted on this page and informed by email.

For queries: Dr. Krishnan C. M. C., Associate Professor, Dept. of E&E. (cmckrishnan@nitk.edu.in)

Sd/-
Nodal Officer, Visvesvaraya PhD Scheme - Phase II
NITK Surathkal