NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल
P.O SRINIVASNAGAR, MANGALORE - 575025

IrIS

EC Call Letter List

PhD, 2026 - 27

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for PhD Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	NISHA ARUNKUMAR NAIK	PH2026EC0050	Electronics and Communication Engineering
2	Kanmani	PH2026EC0058	Electronics and Communication Engineering
3	Lavanya B L	PH2026EC0076	Electronics and Communication Engineering
4	Porika Sairam	PH2026EC0054	Electronics and Communication Engineering
5	KOTESHWAR RAO DANTHAMALA	PH2026EC0062	Electronics and Communication Engineering
6	SAWALE VAISHALI ARVIND	PH2026EC0059	Electronics and Communication Engineering
7	sudha j	PH2026EC0055	Electronics and Communication Engineering
8	Aswan Ramesh	PH2026EC0056	Electronics and Communication Engineering
9	ANUSREE C	PH2026EC0070	Electronics and Communication Engineering

#	Name	Reference Number	Branch/Specialisation
10	Priyamvada	PH2026EC0068	Electronics and Communication Engineering
11	YERLA BHASKARA RAO	PH2026EC0063	Electronics and Communication Engineering
12	Jishna A S	PH2026EC0064	Electronics and Communication Engineering
13	Hanumakonda Hazarathaiah	PH2026EC0069	Electronics and Communication Engineering
14	MUNNANGI KOTI REDDY	PH2026EC0065	Electronics and Communication Engineering
15	YERLA BHASKARA RAO	PH2026EC0066	Electronics and Communication Engineering
16	Chandan V	PH2026EC0067	Electronics and Communication Engineering
17	BIJEESH K	PH2026EC0074	Electronics and Communication Engineering
18	Gopika Gopinath	PH2026EC0072	Electronics and Communication Engineering
19	PALAJI PRUDHVI RAJ	PH2026EC0075	Electronics and Communication Engineering

Sd/-

Head Of Department
Electronics and Communication Engineering

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
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P.O SRINIVASNAGAR, MANGALORE - 575025

IrIS

EC Call Letter List

PhD (MeitY-Visvesvaraya), 2026 - 27

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for PhD (MeitY-Visvesvaraya) Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	Kanmani	PH2026EC0060	Electronics and Communication Engineering
2	GOPIKA GOPINATH E	PH2026EC0073	Electronics and Communication Engineering

Sd/-

Head Of Department
Electronics and Communication Engineering

Dean Academics

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL

P.O. SRINIVASNAGAR, MANGALURU-575 025

No. PhD/2026-27/E6 **Date: 11-11-2026**

Admit Card

Sub: Ph.D. Programme – Written Aptitude Test / Interview for the year 2026-27 (Winter Session)

With reference to your application for admission to Ph.D. Programme in the department of Electronics and Communication Engineering, you are requested to appear for the Written test and Interview at NITK Surathkal. You should produce all the original records such as Date of Birth Certificate, GATE Score Card, Degree Certificate and Marks Cards of all semesters (UG and PG programmes), if the result of the final year exam is awaited then provide a certificate from the head of your institution which states that you are the final year student of (branch) appearing / appeared for the final year / semester examination during academic year 2025-26 and results are awaited, SC/ST/OBC/EWS certificate (if applicable as per proforma), OBC / EWS certificate should be obtained from competent authority as specified in the NITK website and must be dated on or after April 1, 2026, Person with disability certificate (if applicable), Sponsorship letter (if applicable), Conduct Certificate and valid photo identity card. Please keep a self-attested photocopy of all these certificates readily available at the time of interview.

Department	Department of Electronics and Communication Engineering
Place of Reporting	Department of Electronics and Communication Engineering, NITK Surathkal
Document verification	November 30, 2026, 9:00AM onwards
Written Test Date and Time	November 30, 2026, 2:00 PM at ECE Department
Announcement of shortlisted candidates for interview	November 30, 2026, 5:00 PM
Interview Date and Time	December 01, 2026, 9:00AM Onwards, Meeting room, ECE Department. You may have to report at ECE Department on December 02, 2026, If interview formalities are not completed on December 01, 2026.

NOTE:

1. Candidates should be prepared to appear for the written Aptitude Test before the interview. Fee Structure for PhD programme and Course syllabus for aptitude test are provided on Institute's website, i.e. www.nitk.ac.in. A copy of the syllabus for the aptitude test has been appended to this call letter.
2. Full-time/External Registrants - sponsored from Industry or other organizations including Educational Institutions, should have been serving in the sponsoring organisation for a period of at least 2 years after qualifying degree and have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time /External Registrant) in the Institute on **full salary** during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).
3. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage.
4. Please keep the Admit Card ready during the test and interview. You are responsible for safe custody of the Admit Card and in the event of any other person using this Admit Card, the responsibility lies on you to prove that you have not used the service of an impersonator.
5. Please note that no expenses shall be payable for appearing in the written test/Interview.
6. The Selected candidates are required to pay fees and get admitted during the period from 14th to 17th December, 2026.
7. **Candidates must give 10-minute presentations of their research plans (not more than 6 slides). Email the presentation in PDF format to *office.ec@nitk.edu.in* on or before 30th November 2026.**

Sd/ -
Head of the Department

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL
P.O. SRINIVASNAGAR, MANGALURU-575 025
Telephone: 0824-2473046, Website: www.ece.nitk.ac.in

Date: 11-11-2026

Syllabus for PhD Programme Aptitude Test-Winter session 2026

Date and time for:

- **Document verification:** November 30, 2026, 9:00 AM onwards
- **Written Test:** November 30, 2026, 2:00PM to 3:00PM.
- **Interview**: December 01, 2026, 9:00 AM onwards

Question Paper format: 20 MCQ types questions in 60 minutes, with distribution as below:
The Test paper has 2 Parts, Part-1 is compulsory, and Part-2 is stream specific modules. The candidate is supposed to attempt Module A or Module B or Module C from Part-2 depending on the candidature for a particular research domain. Part-1 has 10 multiple choice type questions, whereas, each module of Part-2 has 10 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries -0.25 marks.
Note: Total duration of Exam is one hour. Use of Calculator is permitted.

Part-1:

Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

Part-2:

Module-A (Communication domain)

Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Fundamentals of Signal Processing.

Module-B (Signal Processing and Machine Learning domain)

SP Fundamentals: Time domain analysis of discrete-time systems - Basic discrete time signals, discrete- time Fourier Series, Z Transform – definition and properties, Discrete-time Fourier Series and its properties, Properties and applications of DTFT. Relationship between time, Z- and frequency domains, DFT fundamentals and Properties of DFT, FIR and IIR filters. Data Structure Fundamentals: Algorithm analysis, Asymptotic notations. Divide and Conquer algorithms, Analysis of divide and conquer algorithms, master method, examples - merge sort, quick sort, binary search, Data structures, Linked list, stacks and queues.

Module-C (VLSI Design domain)

Linear and Digital ICs, Digital System Design, VLSI Technology, CMOS VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

Weightage of different academic components for evaluation (As per the information brochure):

Ph.D.

Written test		40%
Technical Interview (60%)	Technical presentation - research aptitude	10%
	Technical interview - subject knowledge	40%
	Research publications	5%
	Experience - research / teaching / industry	5%

Candidates must give 10-minute presentations of their research plans (not more than 6 slides). Email the presentation in PDF format to *office.ec@nitk.edu.in* on or before 30th November 2026.

Sd/-
Head of the Department